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US	20030161192	20030828	47	Nonvolatile semiconductor memory device	365/200			Kobayashi, Toshio et al.	
US	20020105853	20020808	41	Memory access methods and devices for use with	365/233			Naven, Finbar	
US	6556506	20030429	36	Memory access methods and devices for use with	365/233	365/230.03		Naven, Finbar	
B2	US 6418077	20020709	41	Memory access methods and devices for use with	365/233	365/230.03		Naven, Finbar	
B1	US 5959885	19990928	34	Non-volatile memory array using single polv	365/185.07	365/226		Rao, Kameswara K. et al.	
A	US 5949712	19990907	34	Non-volatile memory array using gate breakd	365/185.07	365/226		Rao, Kameswara K. et al.	
A	US 5835402	19981110	34	Non-volatile storage for standard CMOS integ	365/149	365/102; 365/103;		Rao, Kameswara K. et al.	
A	US 4321700	19820323	17	Optical track segment intercept apparatus	369/44.18	359/216; 369/44.34;		Russell, James T.	